

Publications

Farkas-Based Tree-Interpolation

S. Asadi, M. Blicha, A. Hyvärinen, G. Fedyukovich, N. Sharygina.
In 27th Static Analysis Symposium (SAS), 2020.

Incremental Verification by SMT-based Summary Repair

S. Asadi, M. Blicha, A. Hyvärinen, G. Fedyukovich, N. Sharygina.
In Formal Methods in Computer-Aided Design (FMCAD), 2020.

Function Summarization Modulo Theories

S. Asadi, M. Blicha, G. Fedyukovich, A. Hyvärinen, K. Even, N. Sharygina, H. Chockler.
In 22nd International Conference on Logic for Programming Artificial Intelligence and Reasoning (LPAR), 2018.

Lattice-Based Refinement in Bounded Model Checking

K. Even, **S. Asadi**, A. Hyvärinen, H. Chockler, N. Sharygina
In 10th Working Conference on Verified Software: Theories, Tools, and Experiments (VSTTE), 2018.

Computing Exact Worst-Case Gas Consumption for Smart Contracts.

M. Marescotti, M. Blicha, A. Hyvärinen, **S. Asadi**, N. Sharygina.
In International Symposium on Leveraging Applications of Formal Methods (ISoLA), Industrial Practice, 2018.

Theory Refinement for Program Verification

A. Hyvärinen **S. Asadi**, K. Even, G. Fedyukovich, H. Chockler, N. Sharygina.
In 20th International Conference on Theory and Applications of Satisfiability Testing (SAT), 2017.

HiFrog: SMT-based Function Summarization for Software Verification.

L. Alt, **S. Asadi**, H. Chockler, K. Even, G. Fedyukovich, A. Hyvärinen, N. Sharygina
In 23rd International Conference on Tools and Algorithms for the Construction and Analysis of Systems (TACAS), 2017.

Duality-based interpolation for quantifier-free equalities and uninterpreted functions.

L. Alt, A. Hyvärinen, **S. Asadi**, N. Sharygina.
In Formal Methods in Computer-Aided Design (FMCAD), 2017.

HiFrog: Interpolation-based Software Verification using Theory Refinement.

S. Asadi, K. Even, G. Fedyukovich, A. Hyvärinen, H. Chockler, N. Sharygina
In FMCAD Student Forum, 2017.